

Biography:

Dr. Robert H. Caverly received his Ph.D. degree in electrical engineering from The Johns Hopkins University, Baltimore, MD, in 1983. He has been a faculty member at Villanova University in the Department of Electrical and Computer Engineering since 1997 and is a Full Professor. Previously, he was a Professor for more than 14 years at the University of Massachusetts Dartmouth. Dr. Caverly's research interests are focused on the characterization of semiconductor devices such as PIN diodes and FETs in the microwave and RF control environment. He has published more than 100 journal and conference papers and is the author of the book CMOS RFIC Design Principles from Artech House. An IEEE Fellow (2013), Dr. Caverly is an Associate Editor of the IEEE Microwave Magazine and a member of the HF-VHF-UHF Technology (TC-17) and Biomedical Applications (TC-10) Technical Committees of the MTT Society. His webpage is <http://rcaverly.ece.villanova.edu/rcaverly/rcaverly.htm>